

PATENT  
0756-1630

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re PATENT application of: )  
Shunpei YAMAZAKI et al. )  
Serial No.: 08/799,506 ) Group: 2822  
Filed: February 12, 1997 ) Examiner: Wilczewski  
For: METHOD FOR PROCESSING )  
SEMICONDUCTOR DEVICE )  
APPARATUS FOR PROCESSING )  
A SEMICONDUCTOR AND )  
APPARATUS FOR PROCESSING )  
SEMICONDUCTOR DEVICE )

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INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

JULY 17, 2000

Sir:

In accordance with the provisions of 37 C.F.R. § 1.56 and 37 C.F.R. §§ 1.97-1.99, it is requested that the references listed on the attached Form PTO-1449 be made of Record in the above-identified application. Copies of the references are submitted herewith in accordance with 37 C.F.R. § 1.98(a).

This Information Disclosure Statement is being filed under 37 C.F.R. § 1.97(c)(2).

In accordance with 37 C.F.R. § 1.17(p), the prescribed fee of \$240.00 is submitted herewith. A check in the amount of \$240.00 is attached.

Japanese Patent Nos. 01-128446, 02-257619, 04-206532, 05-182923, 57-194518, and 64-076737 were cited in the counterpart Japanese Patent application

(June 6, 2000,) not more than three (3) months to the prior of the Information Disclosure Statement.

Further, Japanese Patent No. 04-206532, which corresponds to U.S. Patent No. 5,294,811, discloses a chamber for depositing a gate insulating film, however, fails to disclose a chamber for depositing the gate insulating film on the semiconductor film.

Applicants draw the Examiner's attention to the following related applications:

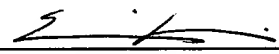
Serial No.	Status
08/721,540	Pending

The Commissioner is hereby authorized to charge fees under 37 C.F.R. § 1.16 and § 1.17 (except the Issue Fee) which may be required now or hereafter, or credit any overpayment, to Deposit Account No. 19-2380. **A duplicate copy of this sheet is attached.**

Respectfully submitted,

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